One-Step Thermo-Chemical Synthetic Method for Nanoscale One-Dimensional Heterostructures

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Inspired by the discovery of carbon nanotubes in 1991,1 enormous research efforts have been made on one-dimensional (1-D) nanostructures, such as nanowires, nanotubes, and nanobelts.2–5 Among them, 1-D nanoscale heterostructures (HS) with modulated structure, compositions, and interfaces have become of particular interest,6–10 which represent an important class of nanoscale building blocks with substantial potential for exploring and realizing novel device applications at the nanometer scale.5–10

Many methodologies have been developed for the synthesis of 1-D HS. For example, two-step or multistep catalytic-assisted metal–organic chemical vapor deposition methods and metal–organic vapor phase epitaxy methods have been extensively used to synthesize diverse 1-D HS, including core/shell, segmented, and hierarchical HS.6–8 Using mixed source material, several 1-D HS were fabricated based on different mechanisms.9 Other methods based on the solution chemical process, electrospinning, lithography, and so forth were also developed to get 1-D HS.10–13 Every methodology has its advantages and disadvantages from different view points. It is still challenging to develop a simple and generatic method to rationally synthesize 1-D nanoscale HS.

Herein, we present a simple one-step thermo-chemical synthetic method for the synthesis of a variety of 1-D HS, including hierarchical C/SiO2 microtube/nanowire, GaP/ZnS nanowire/nanowire, GaP/CdS nanowire/nanowire HS, and core/shell Zn/ZnS, Ge/ZnS nanowires. Our synthetic method is a very simple one with many benefits. It is a general method to produce a large quantity of 1-D HS. With a judicious choice of source materials and careful control of experimental parameters, this simple method can even be extended to synthesize other complex HS, such as self-assembled raspberry-like GaS spheres decorated with Bi nanoparticles.

The one-step thermo-chemical synthetic method for 1-D HS involves a vertical induction furnace14 as shown in Figure 1a. The key feature of the experimental setup is the usage of a specially designed crucible system as marked with a dashed circle in Figure 1a. The crucible system is composed of two graphite or boron nitride crucibles connected with a graphite pipe. The bottom crucible for source A, used to

Figure 1. (a) Schematic illustration of the furnace used in the experiments. (b, c) SEM images of as-synthesized hierarchical C/SiO2 microtube/nanowire HS. (d) TEM image of a hierarchical HS. (e) TEM image of the branched SiO2 nanowires. (f) TEM image taken from the core C microtubes, showing clear lattice fringes.

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produce stem nanowires of desired HS, was set in the center of the heating zone of the furnace, while the position of the upper crucible for source B, used to produce secondary/branched nanowires of HS, is adjustable according to different source materials. To get the desired HS, several factors should be considered when setting the position of upper crucible, such as the melting point of source B and the gas flow rate. In most cases, the graphite crucible was used to promote the evaporation of the source materials, while in some cases, the BN crucible was used to avoid the chemical reaction between graphite and the source materials. The detailed experiments and crucible system setup can be found in the Experiment Section in Supporting Information. Furthermore, a high-frequency vertical induction furnace instead of the conventional tube furnace favors the formation of diverse HS. A main factor is that temperature increases very fast for the induction furnace. For example, the temperature can increase from room temperature to 1400 °C within 10 min. Faster temperature increase is believed to be favorable for rapid evaporation of sources, which is very important for the vapor growth of nanostructures. Besides, the vertical induction furnace has two gas inlets from both the bottom and the top, which is also important for HS synthesis. Our experimental setup fully utilizes the temperature degradation of the furnace and makes the synthetic process finishing within one step.

The first kind of 1-D HS synthesized here is the hierarchical HS. Figure 1b–c demonstrates the hierarchical C/SiO₂ microtube/nanowire HS, which consisted of numerous branched nanowires wrapped around a microtube. The TEM image shown in Figure 1d also shows the hierarchical structure, consistent with the SEM results. The clear brightness constrast indicates the inner part is of tubular structure. Figure 1e is a TEM image of the branched nanowires, which have uniform lengths of 2–3 μm and diameters of 50–100 nm. The clear contrast variations between the tips and the nanowires suggest that the body and the tip of the nanowire may consist of different materials. Energy-dispersive electron spectroscopy (EDS) was used to check the compositions of the hierarchical HS, and the data suggest the core microtubes are C microtubes and the branched nanowires are SiO₂ nanowires with Sn tips. A core GaP nanowire (Figure 2e) shows that it is single-crystalline and the spacing of the lattice planes perpendicular to the nanowire long axis is 0.31 nm, consistent with the (111) planes of cubic GaP. The HRTEM image at the branched small ZnS nanowire gives lattice fringes of 0.63 nm perpendicular to the long axis and 0.38 nm along the long axis, corresponding to the [0001] and [011̅0] lattice fringes of wurtzite ZnS. These data indicate that the core GaP nanowires are single crystals with preferred growth direction along the (111) orientations and the branched ZnS nanowires are single crystals grown along the [0001] direction. Using a similar process, we can also synthesize hierarchical GaP/CdS HS.

The present method is also useful for 1-D core/shell HS. Figure 3a depicts a TEM image of the core/shell Zn/ZnS nanowires. A core/shell structure was easily seen from the contrast variations between the core and the inner part. EDS analysis indicates that it was core/shell Zn/ZnS nanowires. An HRTEM image shown in Figure 3b was used to investigate the microstructures of the Zn core and the ZnS shell, as well as the interface between them. For the upper part, the lattice fringe is about 0.21 nm, corresponding to the (101) plane of hexagonal Zn phase. The measured lattice fringe for the bottom part is 0.63 nm, consistent with the [0001] plane of the wurtzite ZnS phase. The corresponding fast Fourier transport (FFT) patterns of both parts are shown in the Figure 3b inset, revealing the single crystal nature of both the Zn core and the ZnS shells.
mismatch between hexagonal Zn and wurtzite ZnS, high density defects, like stacking faults, exist within the interface of the core and the shells (Figure 3b). Core/shell Ge/ZnS nanowires were also synthesized and Figure 3c is a TEM image of a typical core/shell Ge/ZnS nanowire. Its HRTEM image is depicted in Figure 3d. The marked lattice fringes are 0.33 nm for the Ge core and 0.33 nm for the ZnS shell, respectively. They are obviously consistent with the (111) plane of the cubic Ge phase and the (100) plane of the hexagonal ZnS phase. The interface between the core and the shell is a perfect one and appears to be very homogeneous and atomically sharp. It is easy to explain because of the perfect lattice match between Ge and ZnS. Corresponding FFT patterns of both core and shell were also shown in the Figure 3d inset, which also reveal their single crystal nature.

On the basis of the above results, a general vapor–solid (VS) mechanism was proposed to explain the formation of diverse HS since no catalyst was used in most cases. At high reaction temperature, source A is evaporated directly or reacts with graphite to general vapors for the growth of main nanowires. At the same time, source B is also evaporated (due to the precise control of position of crucible) to generate vapors, which is transferred to low temperature region and deposite on the main nanowires to form secondary/branched nanowires. After reaction finished, 1-D HS are produced. Using GaP/ZnS HS as an example, GaP in crucible A was evaporated at high temperature and the generated GaP gases were transferred to low temperature region and deposite on the inner wall to form GaP nanowires. At the same time, ZnS in crucible B was also evaporated to generate ZnS vapors, which was also transferred to low temperature region and directly deposited on GaP nanowires to form GaP/ZnS HS. Although the formation of C/SiO2 HS is also mainly governed by VS mechanisms for the growth of stem C tubes, catalyst Sn are necessary to promote the growth of secondary SiO2 nanowires, which means a vapor–liquid–solid (VLS) mechanism.

The present one-step thermo-chemical method is also useful to be extended to synthesize other complexes with a judicious choice of source materials and careful control of experimental parameters. Here, self-assembled raspberry-like GaS spheres decorated with Bi nanoparticles were used as an example, and the corresponding discussions are as follows. Figure 4a shows a SEM image of the product synthesized from GaN (Source B) and Bi2S3 (Source A). The product possesses an interesting hierarchical raspberry-like morphology. The morphology can be clearly seen from a high-magnification SEM image in Figure 4b. The raspberry-like sphere consists of a sphere core with many small nanoparticles decorated on it. TEM images of the raspberry-like products were demonstrated in Figure 4c,d, which is similar to the SEM results. EDS analysis reveals that the core is GaS and the decorated small nanoparticles are Bi (Supporting Information, Figure S7). The measured lattice is 0.32 nm for Bi and 0.4 nm for GaS, corresponding to the (012) plane of hexagonal Bi phase and the (004) plane of a hexagonal GaS crystal. The inset FFT (Figure 4g) and SAED patterns (Figure 4h) are consistent with the HRTEM results.

In conclusion, a simple one-step thermo-chemical synthetic method was developed to synthesize a variety 1-D HS. The synthetic 1-D HS include hierarchical HS and core/shell HS. The successful synthesis of 1-D HS makes it possible to investigate their corresponding properties, such as electronic and optoelectronic properties. Devices like field-effect transistors are expected to be fabricated based on single 1-D heterostructures. With a judicious choice of source materials and careful control of experimental parameters, this method was extended to synthesize other complex HS.

Supporting Information Available: Experimental procedures, more SEM, TEM, HRTEM images, and EDS spectra of the produced 1-D HS (Figure S1–S7) (PDF). This material is available free of charge via the Internet at http://pubs.acs.org.